Title: Interleaved Clock Signal Generators:
Inventors: Robert M.R. Neff
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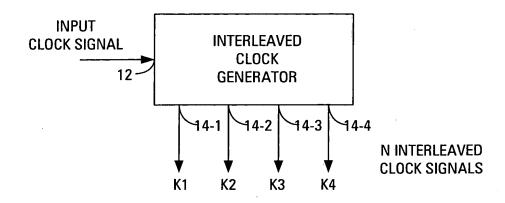


FIG.1A

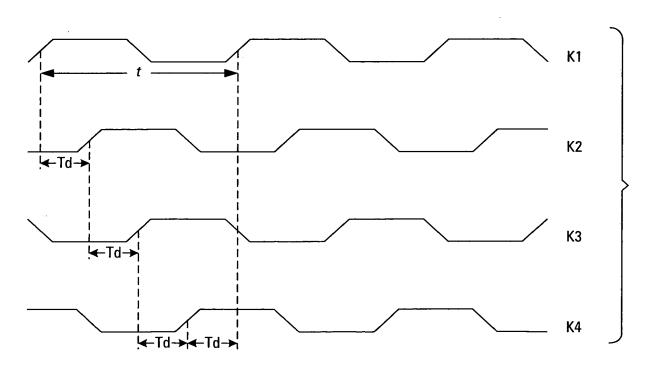


FIG.1B

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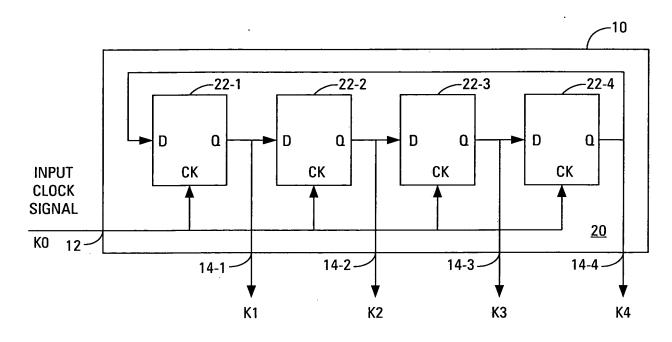


FIG.2A

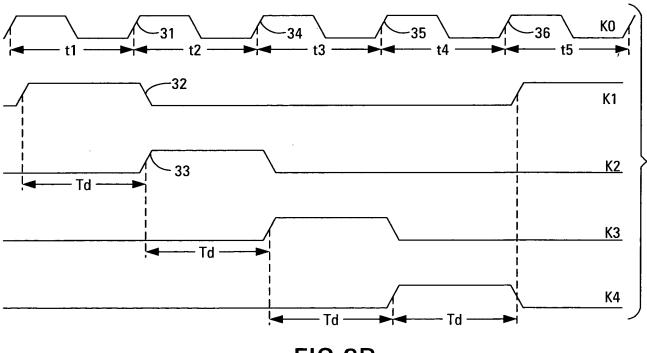


FIG.2B

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-10 **INPUT** -42-1 -42-2 -42-3 -42-4 CLOCK **SIGNAL** D D K0 12 <u>40</u> 14-1-14-2-14-3 14-4 K2 К3 Κ4 **K**1

FIG.3A

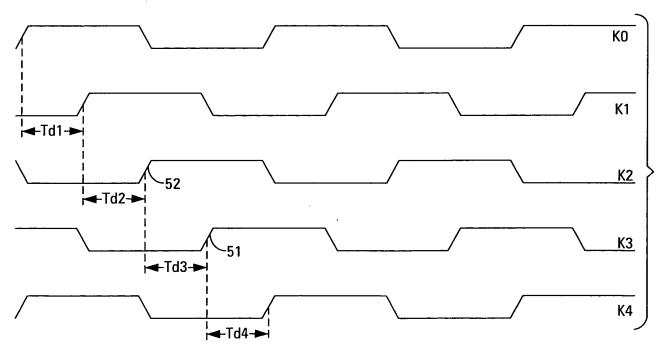


FIG.3B

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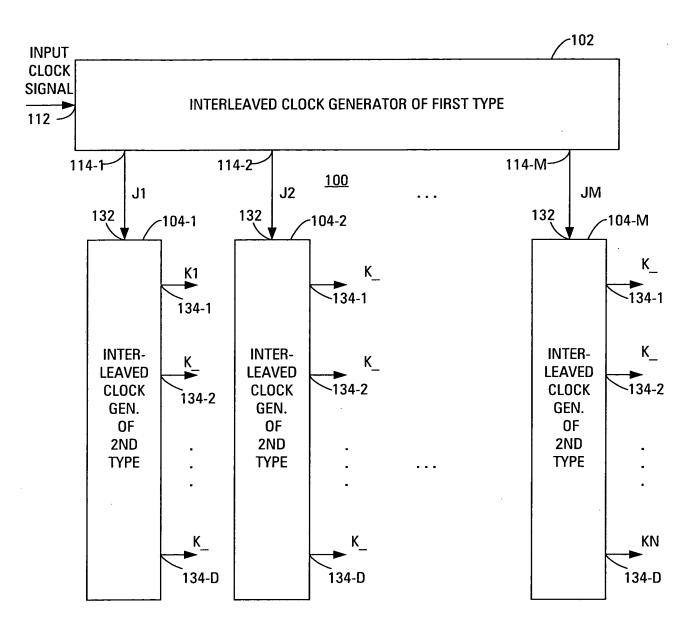


FIG.4

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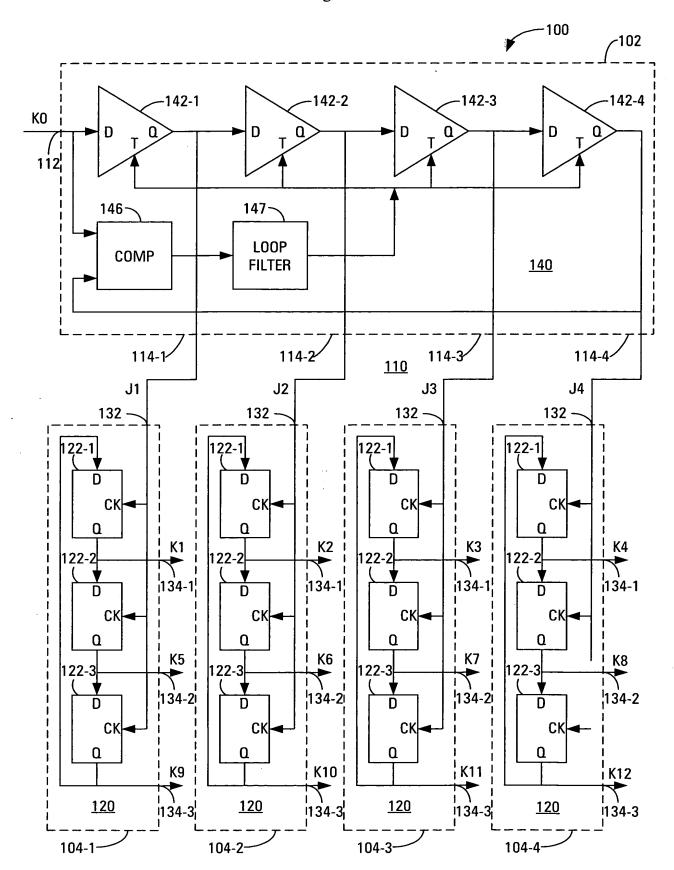


FIG.5A

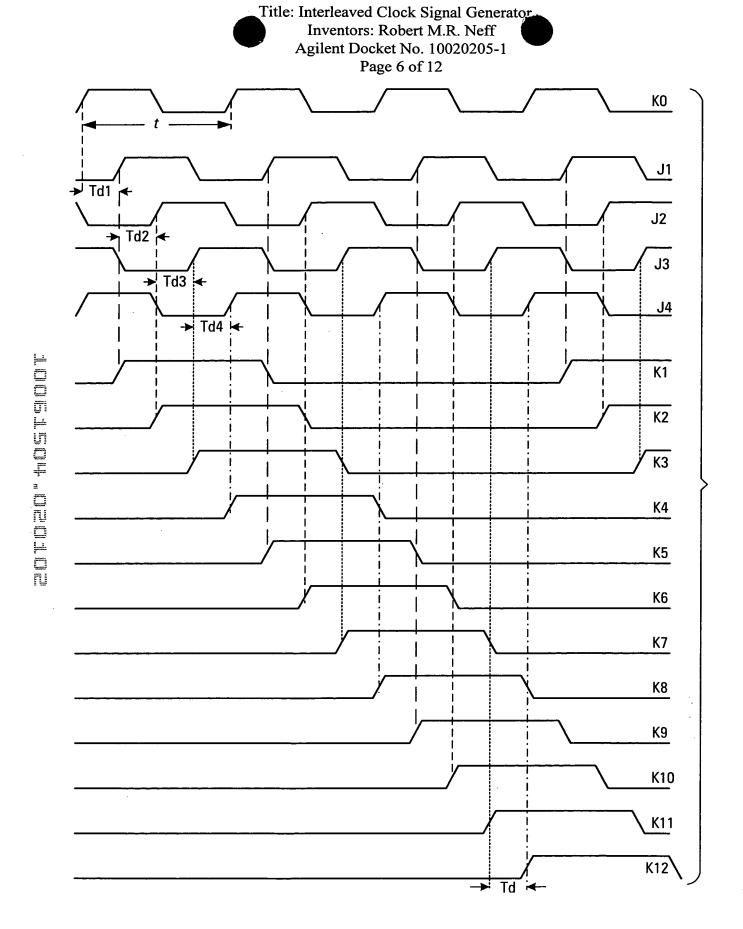
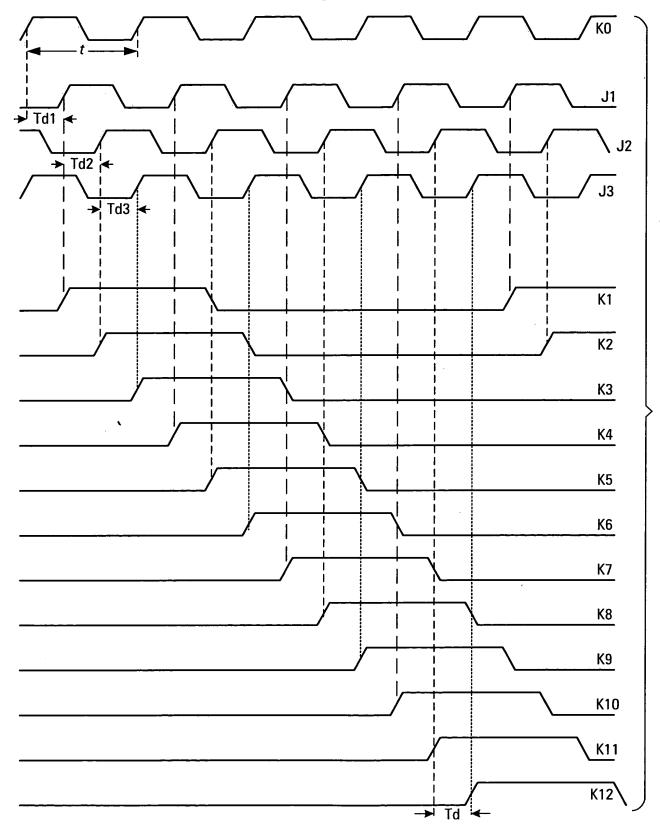


FIG.5B

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HOOGKIOK.OEGHOE

FIG.5C

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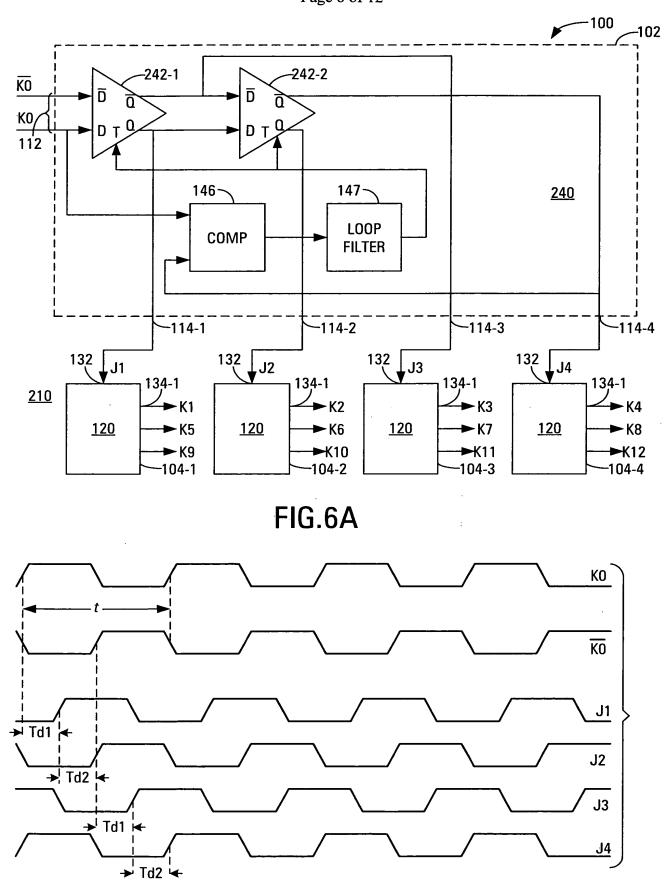


FIG.6B

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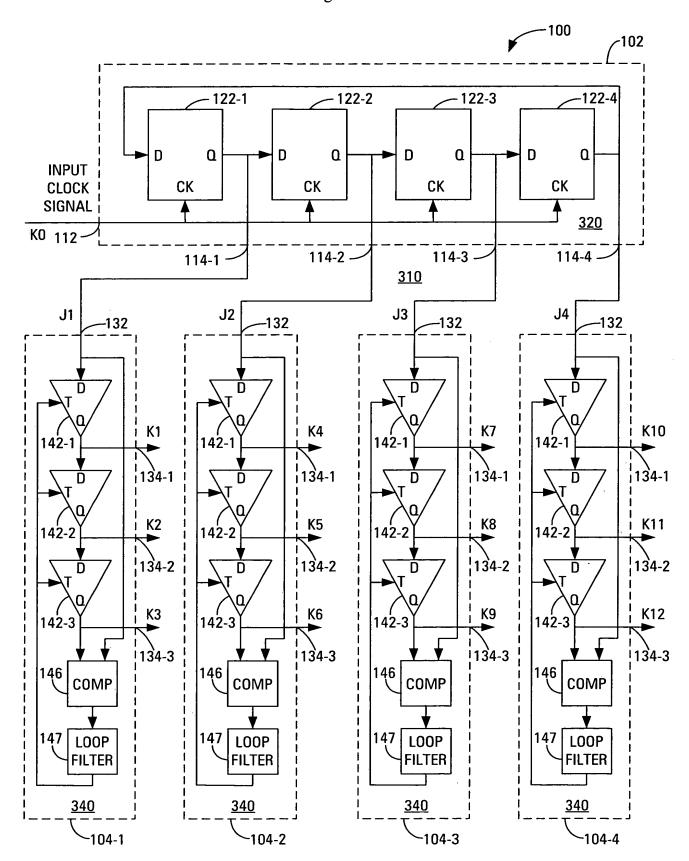


FIG.7A

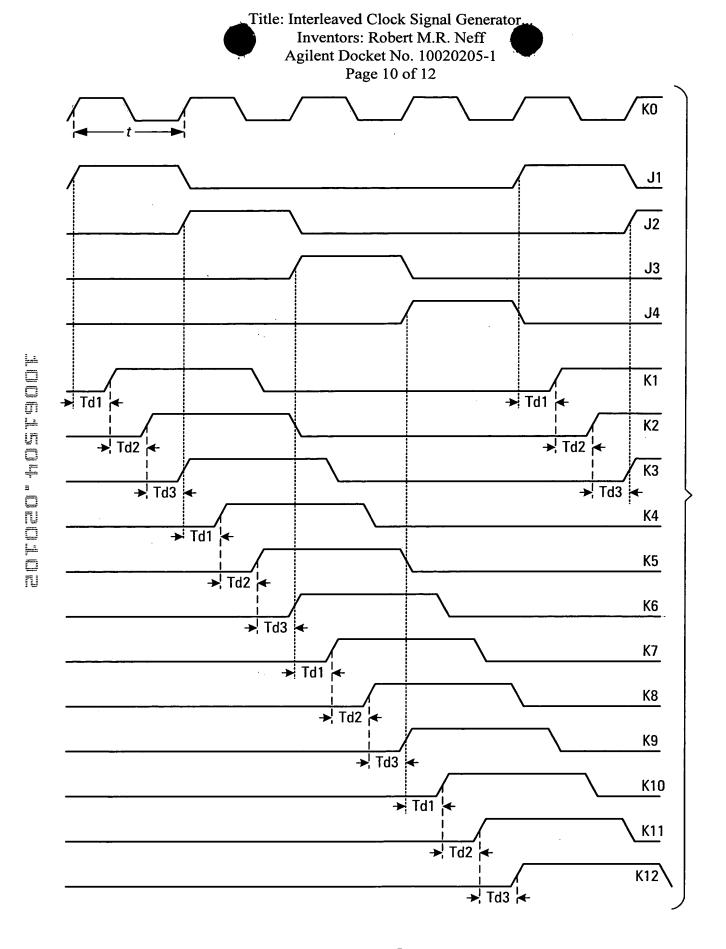


FIG.7B

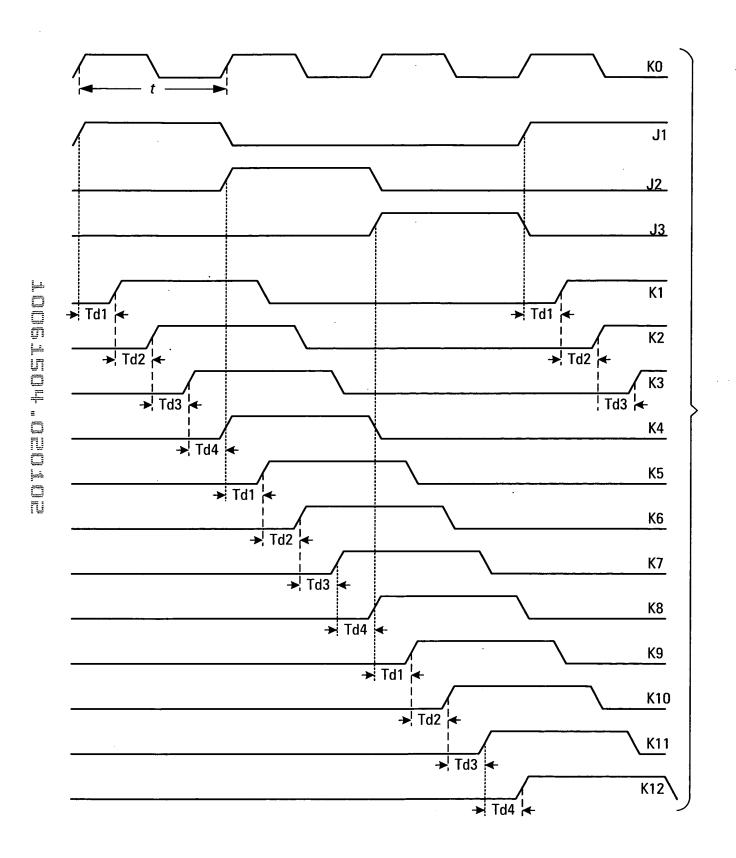


FIG.7C

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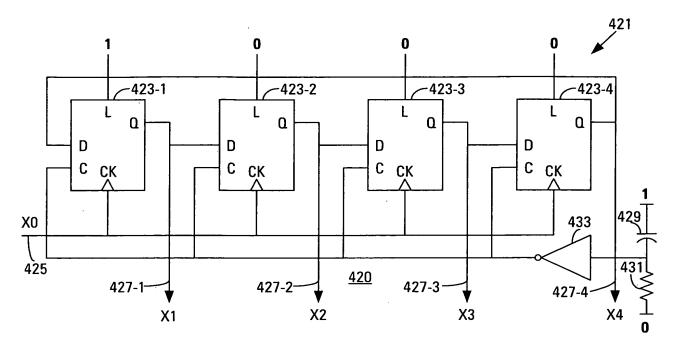


FIG.8A

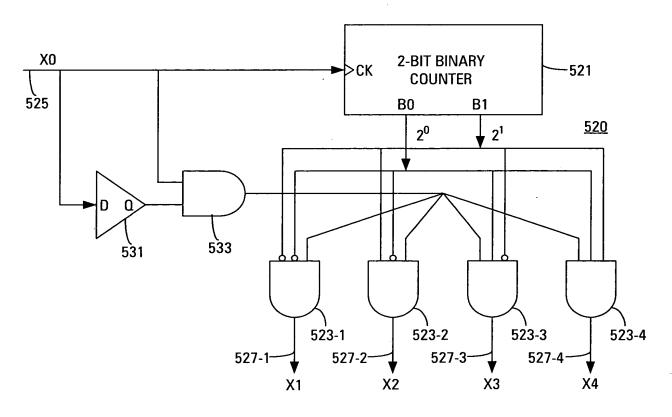


FIG.8B